

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

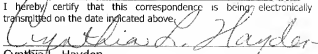
In re Applicant:	§	Art Unit:	2195
Hong Jiang et al.	§	Examiner:	Eric Charles Wai
Serial No.:	§	Conf. No.:	8582
10/750,583	§	Docket:	ITL.1704US
Filed:	§	P17510	
December 31, 2003	§	Assignee:	Intel Corporation
For:	§		
Processing Architecture Having	§		
Passive Threads and Active	§		
Scnaphores	§		

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL REPLY TO FINAL REJECTION**

Sir:

In response to the Advisory Action mailed July 12, 2010, please amend the above-referenced patent application as follows:

Date of Deposit: July 29, 2010  
I hereby certify that this correspondence is being electronically  
transmitted on the date indicated above.  
  
Cynthia L. Hayden